

DL	31	5,270,551	12/14/93	Kamimura et al.				Page 2 of 2
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U.S. Published Patent Application Documents							
*Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	

Foreign Patent Documents						
Document	Date	Country	Class	Subclass	Translation	
					Yes	No

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		

Examiner	<i>DAJ</i>	Date Considered	3/04
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

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INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

APPLICANT

Hock C. So and Sau C. Wong

FILED

October 18, 2001

GROUP

Not Assigned

U. S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
DL	A1	4 4 6 0 9 8 2	7/17/1984	Gee et al.			
	A2	4 6 1 2 6 3 0	9/16/1986	Rosier			
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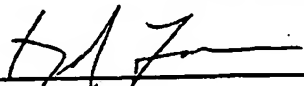
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		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANS? (YES/NO)
DL	B1	WO 9 0 1 2 4 0 0	10/18/199	W.I.P.O.			

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DL	C1	Lee, E.K.F., et al., "Error Correction Technique For Multivalued MOS Memory," <u>Electronic Letters</u> , Vol. 27, No. 15 (July 18, 1991) pps. 1321-1323					

EXAMINER



DATE CONSIDERED

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U.S. Department of Commerce, Patent and Trademark				Atty. Docket No.		Application No.	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				SNDK.176US3		10/045,505	
				Applicant(s)			
Several sheets if necessary)				So et al.		Group	
				Filing Date		2818	
				October 18, 2001			

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